

Amendments to the Claims

Claims 1 and 2 (Canceled)

Claim 3 (Currently Amended) An image processor comprising:

a first storage unit operable to store separated image data formed by several pieces of pixel data, the separated image data having a predetermined width and being separated from lines of image data;

a data readout unit operable to sequentially read out the several pieces of pixel data stored by said first storage unit;

a filtering unit operable to filter target pixel data subject to filtering using predetermined pieces of the pixel data that are read out from said data readout unit, thereby outputting filtered pixel data and non-filtered pixel data, the predetermined pieces of the pixel data including the target pixel data subject to filtering and non-target pixel data subject to filtering;

a second storage unit operable to store the filtered pixel data and the non-filtered pixel data;

a setting unit operable to set a mode signal indicating whether or not the non-filtered pixel data is to be fed; and

a data output control unit operable to control feeding of the non-filtered pixel data according to the mode signal, wherein

~~wherein~~ said first storage unit has a same width as the predetermined width,

the lines of image data have a same width as a line of display, and

the non-filtered pixel data is generated by the non-target pixel data subject to filtering.

Claim 4 (Previously Presented) An image processor as defined in claim 3, wherein the mode signal divides the non-filtered pixel data into first non-filtered pixel data to be fed and second non-filtered pixel data not to be fed.

Claim 5 (Previously Presented) An image processor as defined in claim 3, wherein said data output control unit is operable to feed the filtered pixel data and the non-filtered pixel data according to a timing generated by countdown from an initial value.

Claim 6 (Previously Presented) An image processor as defined in claim 3, wherein said filtering unit comprises an image data-retaining unit operable to retain a number of pieces of pixel data to be used in a next filtering operation, the number of pieces of pixel data to be used in the next filtering operation being selected from among the several pieces of pixel data read out by said data readout unit from said first storage unit.

Claim 7 (New) An image processor as defined in claim 3, wherein the predetermined width is less than the width of the line of display.